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Mechanism analysis of photoleakage current in ZnO thin-film transistors using device simulation

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We analyzed the photoleakage current (I_{leak}) in ZnO thin-film transistors using device simulation. The dependences of I_{leak} on the location of light irradiation and drain voltage are reproduced by considering a Schottky barrier at the source contact using a two-dimensional device simulation. First, carrier generation is induced by light irradiation, the generated holes accumulate near the source contact, and some of these are captured in the donor traps. Next, the Schottky barrier becomes narrow, and electron injection increases via a tunneling effect. This discussion also suggests that the off-current is exceedingly low because the Schottky barrier prevents electron injection. © 2010 American Institute of Physics. [doi:10.1063/1.3502563]

Oxide-semiconductor thin-film transistors (TFTs), such as ZnO TFTs¹ and amorphous In–Ga–Zn–O (α -IGZO) TFTs,² are promising as next-generation giant micro-electronic elements because they are transparent devices, exhibit high performances, and can be fabricated on plastic substrates at low temperatures. Therefore, ZnO TFTs have been actively studied and extensively developed for not only flat-panel displays^{3,4} but also image sensors⁵ and transparent electronics.^{6,7} However, ZnO TFTs are not completely transparent devices even in the visible spectrum from the viewpoint of electrical influences, and a photoleakage current (I_{leak}) is observed upon blue-light irradiation.⁸ The large value of I_{leak} cannot be explained using simple physics of the carrier generation and should be deeply discussed.⁹

In this research, we analyzed the mechanism of I_{leak} in ZnO TFTs using device simulation. The dependences of I_{leak} on the location of light irradiation and drain voltage are reproduced by considering a Schottky barrier at the source contact using a two-dimensional (2D) device simulation. First, carrier generation is induced by light irradiation. Then, the generated holes accumulate near the source contact, and some of these are captured in the donor traps. Next, the Schottky barrier becomes narrow, and electron injection increases via a tunneling effect. This discussion also suggests that the reason the off-current (I_{off}) in ZnO TFTs is exceedingly low is because the Schottky barrier prevents electron injection even if the electron density is relatively high in ZnO films.

The device structures of the ZnO TFTs with light-shield layers to analyze the mechanism of I_{leak} are shown in Fig. 1.⁸ First, a Cr film is deposited as a gate electrode on a glass substrate, a SiN_x film is subsequently deposited as a gate

insulator using plasma-enhanced chemical-vapor deposition (PECVD) of SiH_4 and NH_3 , and a SiO_x film is sequentially stacked using PECVD of SiH_4 and N_2O . Next, a ZnO film is deposited as a channel layer using radio frequency magnetron sputtering at 150 °C, a SiN_x film is sequentially stacked to protect the channel layer, and both films are simultaneously patterned using photolithography and dry etching. Afterwards, a SiN_x film is further deposited as an interlayer insulator, an ITO film is deposited as source-drain electrodes, and a SiN_x film is again deposited as an encapsulation insulator. Finally, a Cr film is deposited as a light-shield layer to restrict light irradiation within the channel layer. Three types of the light-shield layers are formed, where the entire, source-half, or drain-half regions of the channel layer are exposed to light irradiation. Here, the thickness of each film and gate width (W) and length (L) are indicated in Fig. 1.

The dependences of I_{leak} on the irradiation location and drain voltage (V_{ds}) measured using actual ZnO TFTs are

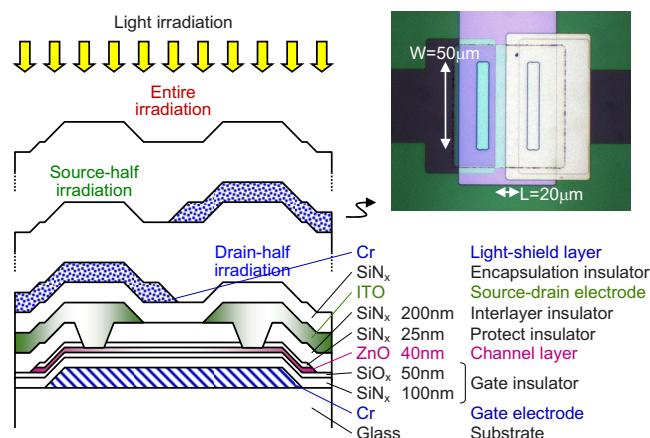


FIG. 1. (Color online) Device structures of the ZnO TFTs with light-shield layers to analyze the mechanism of I_{leak} .

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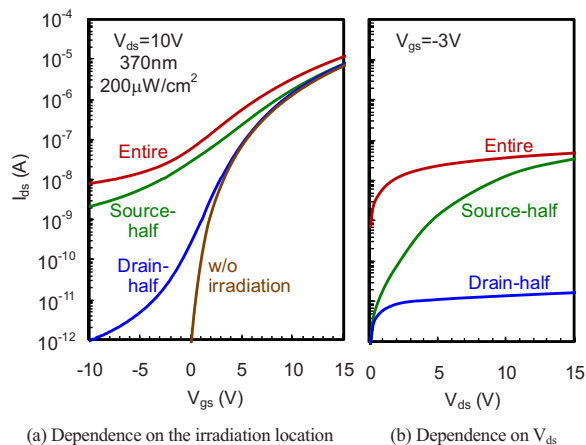


FIG. 2. (Color online) Dependences of I_{leak} on the irradiation location and V_{ds} measured using actual ZnO TFTs.

shown in Fig. 2.⁸ The wavelength and power of light irradiation are 370 nm and $200 \mu\text{W cm}^{-2}$, respectively. Compared to the Si-based TFTs, an outstanding feature is that I_{leak} for the source-half irradiation is larger than that for the drain-half irradiation, as shown in Figs. 2(a) and 2(b). Another feature is that I_{leak} for the source-half irradiation increases as V_{ds} increases, whereas that for the drain-half irradiation does not increase very much even if V_{ds} increases as shown in Fig. 2(b).

The 2D device simulation is executed with carrier generation and tunneling effect models to analyze the mechanism of I_{leak} .¹⁰ Here, we assume a Schottky barrier at the source contact due to the difference in work functions between ZnO and ITO, which is 0.31 eV, and carrier generation is uniformly induced in the corresponding area to the light irradiation, which is $10^{18} \text{ cm}^{-3} \text{ s}^{-1}$ by supposing that the quantum efficiency from the light irradiation to the carrier generation is 1%. The electron mobility is $3.0 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$, which is determined from the on currents of an actual ZnO TFT, and the hole mobility is $10^{-3} \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$, which has a negligible influence on the following conclusion once the value is sufficiently low. The energy gap, electron affinity, dielectric constant, and effective state density in the conduction band are 3.28 eV, 4.29 eV, 8.12, and $4.5 \times 10^{18} \text{ cm}^{-3}$, respectively. The acceptor and donor trap densities are $1.5 \times 10^{17} \text{ cm}^{-3} \text{ eV}^{-1}$ and $3.0 \times 10^{17} \text{ cm}^{-3} \text{ eV}^{-1}$ at the midgap, respectively, which are extracted to fit the subthreshold and on currents without light irradiation.

The dependences of I_{leak} on the irradiation location and V_{ds} simulated using a 2D device simulation are shown in Fig. 3. It is found that the outstanding features above can be at least qualitatively reproduced, although they are not quantitatively accurate, which may be owing to the aforementioned assumptions. However, we think that the 2D device simulation correctly implements the mechanism of I_{leak} .

The spatial profile of the charge density near the source contact is shown in Fig. 4. Although that for the source-half irradiation is shown, the shapes for the entire and drain-half irradiation are similar except the values of the charge density. It is found that the charge density is high at the lower insulator interface. The positive charge originates from the hole carriers and donor traps. Moreover, the charge density for the entire irradiation is similar to that for the source-half irradiation. Both these charge densities are much higher than that

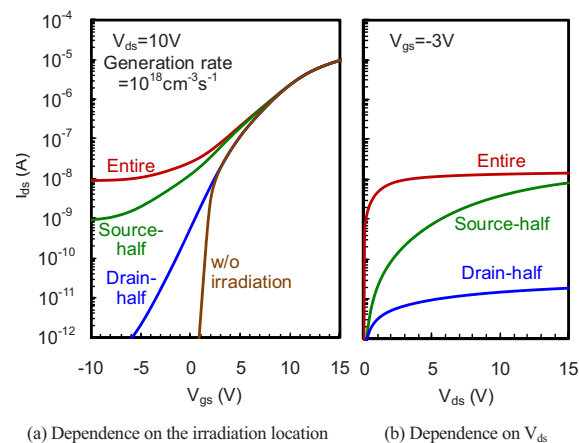


FIG. 3. (Color online) Dependences of I_{leak} on the irradiation location and V_{ds} simulated using a 2D device simulation.

for the drain-half irradiation, which is of course much higher than that without irradiation.

The dependences of the energy band across the source contact on the irradiation location and V_{ds} are shown in Fig. 5. Here, the spatial profile of the conduction band energy (E_c) along the dashed line in Fig. 4 is plotted. It is found that the Schottky barrier for the entire irradiation is narrower than that for the source-half irradiation, which is also narrower than that for the drain-half irradiation, whereas that without irradiation is terribly high. Moreover, the Schottky barrier for the source-half irradiation becomes narrower as V_{ds} increases.

The mechanism of I_{leak} deduced from the measured and simulated results is shown in Fig. 6. First, carrier generation is induced by light irradiation. In the case of the source-half irradiation, generated holes are gathered and accumulate at the lower insulator interface near the source contact because the hole mobility is very low, and some of these are captured in the donor traps. Next, the positive charge lowers the energy band, the Schottky barrier becomes narrow, and electron injection is subject to a tunneling effect. Therefore, I_{leak} for the source-half irradiation is larger. Moreover, the Schottky

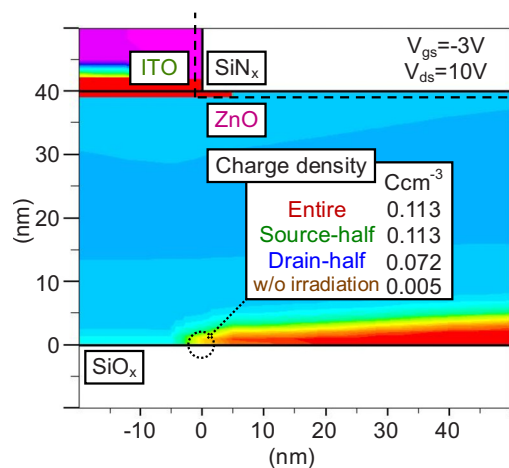


FIG. 4. (Color online) Spatial profile of the charge density near the source contact.

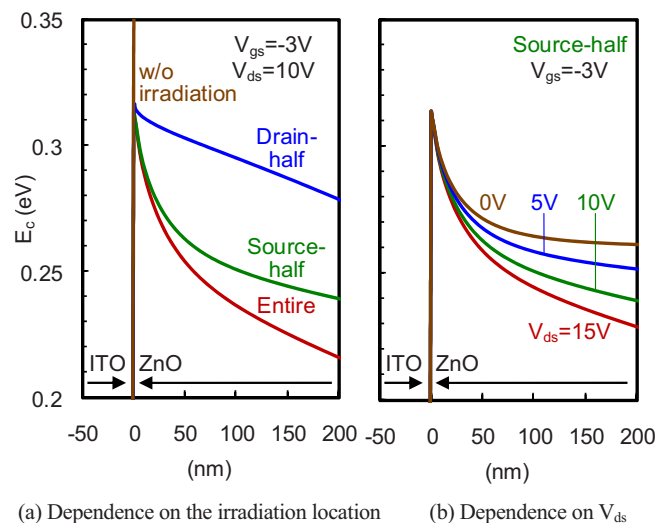


FIG. 5. (Color online) Dependences of the energy band across the source contact on the irradiation location and V_{ds} .

barrier becomes narrower by applying V_{ds} , and the tunneling effect is enhanced. Therefore, I_{leak} for the source-half irradiation increases as V_{ds} increases, which is similar to the drain induced barrier lowering and can be named drain induced

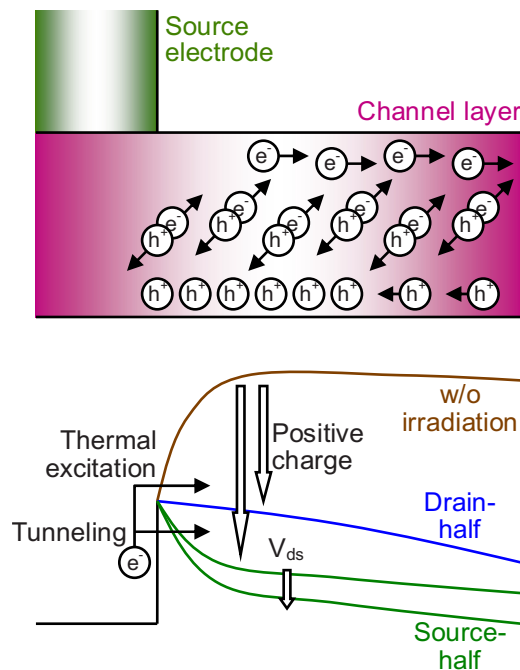


FIG. 6. (Color online) Mechanism of I_{leak} deduced from the measured and simulated results.

Schottky barrier narrowing.¹¹ In the case of the drain-half irradiation, generated holes recombine during the long-time transport from the drain-half region to the source contact because hole mobility is low. The Schottky barrier remains wide, and electron injection is subject to thermal excitation. Therefore, I_{leak} for the drain-half irradiation is smaller. Moreover, the electron injection via thermal excitation depends only on the height of the Schottky barrier and not the width. Therefore, the increase in I_{leak} for the drain-half irradiation is small even if V_{ds} increases.

This discussion also suggests that the reason I_{off} in ZnO TFTs is exceedingly low is because the Schottky barrier prevents electron injection even if the electron density is relatively high in ZnO films. The Schottky barrier may become wider and prevent electron injection by applying a negative gate voltage (V_{gs}) before the electron density is sufficiently reduced in ZnO films.

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- ¹T. Hirao, M. Furuta, T. Hiramatsu, T. Matsuda, C. Li, H. Furuta, H. Hokari, M. Yoshida, H. Ishii, and M. Kakegawa, *IEEE Trans. Electron Devices* **55**, 3136 (2008).
- ²K. Nomura, H. Ohta, A. Takagi, T. Kamiya, M. Hirano, and H. Hosono, *Nature (London)* **432**, 488 (2004).
- ³T. Hirao, M. Furuta, H. Furuta, T. Matsuda, T. Hiramatsu, H. Hokari, M. Yoshida, H. Ishii, and M. Kakegawa, *J. Soc. Inf. Disp.* **15**, 17 (2007).
- ⁴S.-H. K. Park, C.-S. Hwang, M. Ryu, S. Yang, C. Byun, J. Shin, J.-I. Lee, K. Lee, M. S. Oh, and S. Im, *Adv. Mater. (Weinheim, Ger.)* **21**, 678 (2009).
- ⁵S. Aihara, H. Seo, M. Namba, T. Watabe, H. Ohtake, M. Kubota, N. Egami, T. Hiramatsu, T. Matsuda, M. Furuta, H. Nitta, and T. Hirao, *IEEE Trans. Electron Devices* **56**, 2570 (2009).
- ⁶R. L. Hoffman, B. J. Norris, and J. F. Wager, *Appl. Phys. Lett.* **82**, 733 (2003).
- ⁷E. Fortunato, P. Barquinha, A. Pimentel, A. Gonçalves, A. Marques, L. Pereira, and R. Martins, *Thin Solid Films* **487**, 205 (2005).
- ⁸Y. Kamada, S. Fujita, T. Hiramatsu, T. Matsuda, H. Nitta, M. Furuta, and T. Hirao, *Jpn. J. Appl. Phys.* **49**, 03CB03 (2010).
- ⁹Y. Kamada, S. Fujita, T. Hiramatsu, T. Matsuda, M. Furuta, and T. Hirao, *Solid-State Electron.* **54**, 1392 (2010).
- ¹⁰Atlas, TFT, Blaze, and Luminous, Device Simulator, Silvaco International.
- ¹¹See supplementary material at <http://dx.doi.org/10.1063/1.3502563> for some questions about the mechanism of I_{leak} .